

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Programmable Power Supply System
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Application Number :

Confirmation Number:

First Named Applicant: Malachy Devlin

Attorney Docket Number: 021230-000120US

Art Unit: 2819

Examiner: Don P Le

Search string: (4348636 or 6625073 or 6351827 or 6242943 or 5790469 or 20020049941).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
W	1	4348636	1982-09-07	Doundoulakis			
	2	6625073	2003-09-23	Beffa			
	3	6351827	2002-02-26	Co et al.			
	4	6242943	2001-06-05	El-Ayat			
W	5	5790469	1998-08-04	Wong			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
W	1	20020049941	2002-04-25	Lunde et al.			

Signature

Examiner Name	Date
Don P Le	8/27/04

PTO/SB/08B (08-03)

Substitute for form 1448B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	10/708,753
		Filing Date	March 23, 2004
		First Named Inventor	Malachy Devlin
		Art Unit	2818
		Examiner Name	Don P. Le
Sheet 1 of 1	Attorney Docket Number	021230-000120US	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
W	A	"EL7656BC Integrated Adjustable 6 Amp Synchronous Switcher," Elantec Semiconductor, Inc. data sheet, August 30, 2000, pp. 1-13.	
	B	Devlin, Malachy, et al., "DIME - The first module standard for FPGA based High Performance Computing," Proceedings of FPL'99, Glasgow, UK Sept. 1999, published as Springer LNCS 1673, 6 pages.	
	C	"MAX4598 Low-Voltage, Combination Single-Ended 8-to-1/Differential 4-to-1 Multiplexer," Maxim Integrated Products data sheet, October 1998, pp. 1-12.	
	D	"DIME Module, Physical Level 0 Specification - Issue 4 NT301-0001," Nallatech Ltd. data sheet, November 23, 2000, pp. 1-21.	
	E	"Distributed DSP Processing, Level 1 Implementation of the DIME Module Standard NT301-0002," Nallatech Ltd. data sheet, April 22, 1999, pp. 1-13.	
he	F	"Virtex™ -E 1.8 V Field Programmable Gate Arrays," Xilinx DS022 (v 1.2) Advance Product Specification, January 28, 2000, pp. 1-190.	
	G		
	H		
	I		
	J		
	K		

Examiner Signature	Don P. Le	Date Considered	5/27/04
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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